



Docket No.: M4065.0761/P761
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Chandra Mouli, et al.

Application No.: 10/602,721

Confirmation No.: 9931

Filed: June 25, 2003

Art Unit: 2811

For: REDUCED BARRIER
PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH
EFFICIENCY CHARGE TRANSFER AND
REDUCED LAG AND METHOD OF
FORMATION

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicants, a copy of which is attached hereto, has the following errors:

The title should read: Reduced Barrier Photodiode/Transfer Gate Device Structure For High Efficiency Charge Transfer And Reduced Lag And Method Of Formation; and

The attorney docket no. should read: M4065.0761/P761.

Application No.: 10/602,721

Docket No.: M4065.0761/P761

Applicants additionally request that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: November 24, 2003

Respectfully submitted,

By 

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UNITED STATES PATENT AND TRADEMARK OFFICE

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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
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| 10/602,721 | 06/25/2003 | 2811 | 1602 | N4065.0761/P761 M4065.0761/P761 | 8 | 58 | 5 |

CONFIRMATION NO. 9931

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FILING RECEIPT



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NOV 10 PM 2:21

Date Mailed: 11/05/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 11/04/2003

Projected Publication Date: To Be Determined - pending completion of Corrected Papers

Non-Publication Request: No

Early Publication Request: No

Title

TRANSFER
Reduced barrier photodiode/gate device structure for high efficiency charge transfer and reduced lag and method of formation

Preliminary Class